

PEIE 2010 Accepted Full Paper List

Paper ID	Title	Author(s)
PEIE 11	Simple Variable Speed Drive for Single-Phase Induction Motor	Rakesh Babu Panguloori, Prasad Dinkar
PEIE 17	Iterative Clipping and Filtering Method for PAPR Reduction in OFDM	Khyati Desai, Ms. Jigisha Patel
PEIE 18	A New Bicmos Constant Current Source	Raghuveer.M
PEIE 29	Digitally programmable Multifunctional Filter and Multiphase Oscillator using MOCCCII	Ale Imran Muzaffer.A.Siddiqi
PEIE 31	Design of High Frequency Low Power CMOS Dual- Output Current Conveyor at 32nm technology node	Ale Imran M.W.Akram Mohd. Hasan
PEIE 32	Synchronization Between Two Different Chaotic Systems Using Adaptive Sliding Mode Control	Zahra Rabiei, Naser Pariz, Gholamreza Bideri
PEIE 33	Novel Design For RF MEMS Capacitive Shunt Switch in K and Ku Bands	Rakesh S Lal, A Amalin Prince, Iven Jose
PEIE 34	Analysis and Design of Controller for Two Area Thermal-Hydro-Gas AGC system	Kiran Kumar Challa, Nagendra Rao P S,
PEIE 41	Novel Memory Reference Reduction Methods for FFT Implementations on FPGA	Shrikant S.Chamlate Pankaj Joshi
PEIE 43	An Improved Multiple Output Forward Converter Topology	Lav Kumar Kaushik, M.K.Pathak
PEIE 46	Power System Dynamic Stability Enhancement of SMIB Using Fuzzy Logic Based Power System Stabilizer	Kamalesh Chandra Rout, Dr.P.C.Panda
PEIE 47	Modeling and Simulation of PV Powered Hybrid Thermo-Electric Refrigerator	K. Prabhath, S. Kumaravel and S. Jayaraj
PEIE 49	Transmission Expansion Planning In Deregulated Environment	Hari Priya
PEIE 51	Optimal Design of Multi-machine Power System Stabilizer Using Biogeography-Based Optimization	Mukesh Kumar Das
PEIE 54	A Novel Configuration of Unified Power Flow Controller	Baskar.S, Kumarappan.N, Gnanadass.R
PEIE 57	Hybrid Systems Approach for Networked Control Systems	Seshadhri Srinivasan and Ramakalyan Ayyagari
PEIE 504	New Results on the Global Chaos Synchronization for Liu-Chen-Liu and Lü Chaotic Systems	Dr. V. Sundarapandian and Mr. R. Suresh

PEIE 505	Reduced Order Model Design for the Linear Discrete-Time Control Systems	V. Sundarapandian, M. Kavitha and C.S. Ravichandran
PEIE 513	Conventional PSO [Particle Swarm Optimization] For Solving Dispatch Problem With Considering Generator Constraints	K. K. Saini Mrs. Sanju Saini
PEIE 514	Voltage optimization for Temperature Variation Insensitive, Energy-efficient CMOS Circuits in DSM Technology	D.Chirangeevi S.Govindarajulu, Dr. T.Jayachandra Prasad
PEIE 515	Distance relays performance on EHV Transmission line during Power swings.	Harikrishna M

PEIE 2010 Accepted Short Paper List

Paper ID	Title	Author(s)
PEIE 6	Jpeg Compressed Image Transmission Usingunequal Power Allocation (Upa) Over Mimo-Ofdm Systems	R.Deepa, K.Karthik, Meera Vijayakumar, V.Senthil Kumar, B.Siddharth, B.Pravin Kumar
PEIE 12	Design of Cranial Electrotherapy Stimulator for Boosting Memory Power and analyzing it with EEG	N.GopalaKrishnan Mahesh Veezhinathan
PEIE 24	Analysis on Static Voltage Stability Using SVC, STATCOM, TCSC & UPFC	S.M. Padmaja, Dr. G. Tulasiram Das,
PEIE 39	Torque ripple minimization of Permanent magnet brushless DC motor using Genetic Algorithm	E.Kaliappan C.Sharmeela
PEIE 45	Residential-Scale Solar/Pico-Hydel/Wind Based Hybrid Energy System for Remote Area Electrification	Kumarvel S, Dr. Ashok S
PEIE 48	Designing CDMA Modem in FPGA Based on DSSS	Amit Tripathi, M.S. Korde
PEIE 507	Voltage Clamped Dc-Dc Converter With Reduced Reverse Recovery Current And Switch Voltage Stress	P.Hari Krishna Prasad Dr.Venu Gopala Rao Mannam
PEIE 508	State of charge determination of lead acid battery using metal detector	M. H. Hedayati, P. S. Nagendra Rao
PEIE 510	Secure Voice Communication Using Low Bit Rate Systems For GSM Users	Jasminfeminapriyadars hini.M, Ankur Sood, Y.N.Suresh Reddy

PEIE 2010 Accepted Poster Paper List

Paper ID	Title	Author(s)
PEIE 1	Vulnerable Load Bus Identification Using Radial Basis Neural Network	Gauri Shankar, Bhavik Suthar, R. Balasubramanian, Prince. A
PEIE 5	Modelling And Control Of Five-Phase Induction Motor Drive	J.Bala Krishna
PEIE 9	Design of Viterbi Decoders with Reduced Power Consumption	Bijoy Goswami, S.K Gupta, R.H. Laskar,
PEIE 16	FPGA Based Design of Robust Spatial Domain Image Watermarking Algorithm	Basu Abhishek, Das Tirtha Sankar, Sarkar Subir Kumar
PEIE 19	A ZCS PWM For Half-Bridge Inductive DC–DC Isolated Converters for Fuel Cell Applications.	Dinesh Shetty
PEIE 22	A Novel Multiple License Plate Extraction for Complex Background using Edge Density and Plate Companionable Masking in Indian Traffic Conditions	Chirag N. Paunwala, Suprava Patnaik
PEIE 25	Implementation of Flash ADC Using ROM Array and Multiplexer Based Circuit	Meghana Kulkarni, V. Sridhar , G.H. Kulkarni
PEIE 26	Fuzzy Reactive Navigation Technique of Mobile Agent	Kundu Shubhasri, Parhi Dayal R.
PEIE 30	Design Of 2 To 1 Input Mux Based Barrel Shifter Using Closed Transmission Gate Adibatic Logic	Chandrabhushan Pal
PEIE 36	A High Performance Direct Torque Control of PMBLDC motor using Hybrid (GA based Fuzzy logic) controller	E.Kaliappan, C.Sharmeela, Sayee Krishna.A.V
PEIE 37	Embedded System Design and Real Time Hardware Implementation of OLED Interface Card	Himanshu Singh, Sudhir Khare, Ajay Kumar, S S Negi.
PEIE 42	Frequency Detector using Piezo laminated cantilever beam	Santhosh Kumar B.V.M.P, Varun Kumar U, Suresh K, Uma G Umapathy M